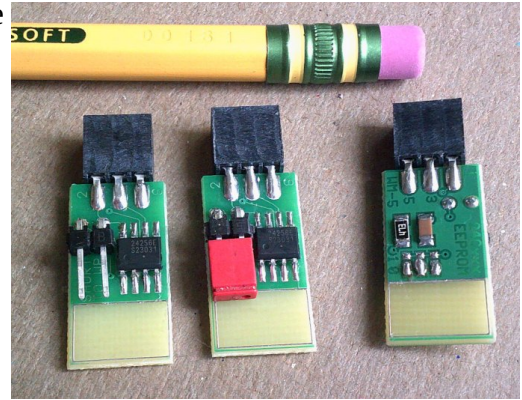


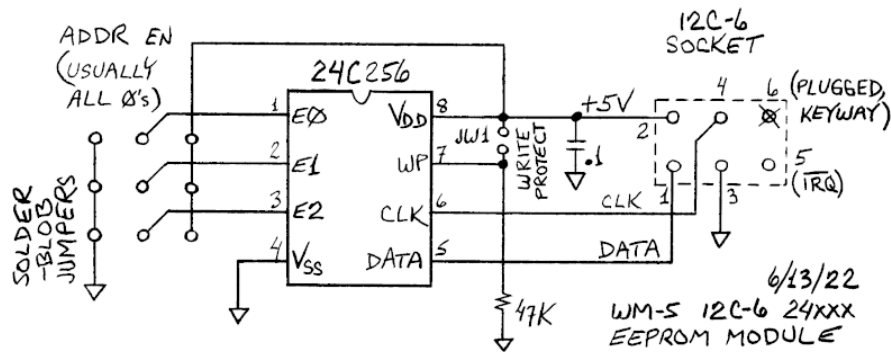
# WM-5 24256 32Kx8 I<sup>2</sup>C EEPROM module

- Uses OnSemi [CAT24C256](#) I<sup>2</sup>C EEPROM in SO-8 package (but boards could be assembled with other pin-compatible EEPROM variations).
- 2-pin header to plug shorting bar onto for write-protect
- .450"x1.225" including socket and pull-tab/label area
- suitable for bootload code or other data totaling up to 32KB
- hobbyist-friendly, keyed [I2C-6 connector](#)
- I<sup>2</sup>C is very easy to bit-bang if necessary. There's sample code in multiple forms at <http://wilsonminesco.com/6502primer/GENRLI2C.ASM>
- "Solder-blob" jumpers on the back of the PCB for A0, A1, and A2 are set for 000, but can be changed.
- Thin, grounded ring around the pull-tab area reduces risk of ESD damage.



The picture above shows two right side up, and one upside down. The second one has a red write-protect jumper on it.

Here's the schematic:



and the board layout:

